PTO/SB/21 (09-04)
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U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number. **Application Number** 09/927,139 **TRANSMITTAL** Filing Date August 10, 2001 **FORM** First Named Inventor Gupta, Gagan V. Art Unit 2124 **Examiner Name** Ngo, Chuong D. (to be used for all correspondence after initial filing) Attorney Docket Number 01-074

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ENCLOSURES (Check all that apply)									
V	Fee Transmittal Form	V	Drawing(s)			After A	Illowance Communicatio	n to TC	
	Fee Attached		Licensing-related Papers				opeal Communication to Board Appeals and Interferences	rd	
	Amendment/Reply After Final Affidavits/declaration(s) Extension of Time Request Express Abandonment Request Information Disclosure Statement		Petition Petition to Convert to a Provisional Application Power of Attorney, Revocati Change of Correspondence Terminal Disclaimer Request for Refund CD, Number of CD(s) Landscape Table on C	Address		Proprie Status	Enclosure(s) (please Ide		
	Certified Copy of Priority Document(s) Reply to Missing Parts/ Incomplete Application Reply to Missing Parts under 37 CFR 1.52 or 1.53		Remarks 1. Power of Attorney and Correspondence Address Indication Form, PTO/SB/81 2. Statement Under 37 CFR 3.73(b), PTO/SB/96 3. Fee Transmittal, Part B 4. Return Postcard						
SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT									
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Printed name Sandeep Jaggi / S									
Date		10/	104	Reg. No.	43,331				
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S PATENT AND TRADEMARK OFFICE IN THE UNITED SA

In re Application of:

Gagan V Gu

Mengchen Yu

Serial No.:

09/927,139

Filed:

August 10, 2001

For:

Floating Point/Integer Divide &

Square Root Processor

Group Art Unit:

2124

Examiner:

Ngo, Chuong D.

Atty Docket:

L13.12-0159 / 01-074

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Mark Salvatore

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October 13 2004

Signature

SUBMISSION OF FORMAL DRAWINGS PURSUANT TO 37 C.F.R. §1.85

Official Draftsman

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicant hereby substitutes the enclosed formal drawings for those presently in the above referenced application.

LSI Logic Corporation 1621 Barber Lane, MS D-106 Milipitas, CA 95035 408-433-7475

Date: 10/11/04

Respectfully submitted,

Sandeep Jaggi

Reg. No. 43,331